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Mixed analog-digital VLSI devices and technology an introduction, Yannis Tsividis, 1996, Computers, 284 pages. . Instructor's Solution Manual for Operation and Modeling of the Mos 3rd Ed , Yannis Tsividis, Colin McAndrew, Dec 14, 2010, Metal oxide semiconductor field-effect transistors, 248 pages. .

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A MOS transistor is a 4-terminal device on a silicon substrate [Martin 2000]. Circuit schematic diagrams often show transistors in 3-terminal symbols, with the assumption that the fourth terminal (known as the substrate terminal) is either grounded or connected to power

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supply on the basis of the device type.

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